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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **YAMAGISHI, Yasuo, et al.**

Group Art Unit: 2829

Serial No.: 10/621,445

Examiner: **HOLLINGTON, Jermele M.**

Filed: **July 18, 2003**

P.T.O. Confirmation No.: 1112

**FOR: PROBE CARD AND TESTING METHOD OF SEMICONDUCTOR CHIP,
CAPACITOR AND MANUFACTURING METHOD THEREOF**

RESPONSE TO ELECTION OF SPECIES REQUIREMENT
DATED NOVEMBER 10, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: December 1, 2004

Sir:

In response to the Election/Restriction Requirement dated **November 10, 2004**, the applicants provisionally elect to prosecute the claims of Group I, Claims 1-24, and the species embodying Fig. 3. Claims 1-11 read on this group/species. The applicants reserve the right to file divisional applications on the non-elected claims.

The applicants respectfully request an early examination on the elected claims 1-11 and favorable action on the merits. If the Examiner has any questions with this Response, please feel free to call the undersigned at the indicated telephone number.

In the event that this paper is not timely filed, applicants hereby petition for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

In the event any additional fees are required in connection with this response, please
charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS,
HANSON & BROOKS, LLP



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PATENT TRADEMARK OFFICE